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Supermicro 4-way 1U Quad-Core AMD Opteron(TM) 8000 Platforms Once Again Selected for Large HPC Cluster Project

Superior Energy Efficiency, High Memory Capacity and Performance Optimization

SAN JOSE, Calif., Nov 14, 2007 /PRNewswire-FirstCall via COMTEX News Network/ -- Super Micro Computer, Inc. (Nasdaq: SMCI), a leader in application optimized, high performance server solutions, today announced that its Quad-Core AMD Opteron(TM) 8000 (Barcelona) platforms were recently selected for the prestigious Tri-Laboratories Linux Capacity Cluster 2007 (TLCC07) Project. Key design advantages enabling Supermicro's continued technology leadership include compute density, high memory capacity, superior energy efficiency, and performance optimization.

"Our previous successful quality and delivery on a similar project with over 3,000 nodes paved the way for this one," said Charles Liang, CEO and president of Supermicro. "Ideal for HPC clusters, Supermicro server platforms deliver unsurpassed performance and energy efficiency, which not only helps reduce total cost of ownership (TCO) but also helps preserve our environment for future generations."

"As the world's leading high performance scientific program, Advanced Computing and Simulation is looking to meet an ever-growing demand for computing resources, it's imperative we find ways to dramatically reduce HPC cluster total cost of ownership," said Mark Seager, head of new computing technologies for Lawrence Livermore National Laboratory. "With these server platforms, we build on previous Tri-Lab successes with Linux clusters to provide National Nuclear Security Administration scientists with even more cost-effective computational tools for fulfilling their national security missions."

The TLCC07 project deploys both 16 DIMM and 32 DIMM Supermicro 4P platforms for up to 96.8TB of memory. High-efficiency voltage regulator modules (VRMs) save energy consumption in each of the 12,096 processors and 48,348 cores. This significantly reduces energy costs on all 3,024 nodes. High-efficiency power supplies in the Supermicro chassis further increase overall system power savings.

Other design features for performance optimization include dual HyperTransport(TM) links for increased system bandwidth and performance, as well as Dual Dynamic Power Management (DDPM) for increased performance and improved power management. Supermicro is working closely with partners such as Appro to deploy these high performance clusters in the tri-Labs community, which includes LLNL, Los Alamos and Sandia National Labs, starting in late 2007 through early 2008.

Supermicro Server Building Block Solutions(R) offer exceptional flexibility and outstanding feature advantages. For more information on Supermicro's complete line of server and workstation solutions go to <http://www.supermicro.com>.

About Super Micro Computer, Inc.

Established in 1993, Supermicro emphasizes superior product design and uncompromising quality control to produce industry-leading serverboards, chassis and server systems. These mission-critical Server Building Block solutions provide benefits across many environments, including data center deployment, high-performance computing, high-end workstations, storage networks and standalone server installations. For more information on Supermicro's complete line of advanced motherboards, SuperServers, and optimized chassis, visit <http://www.Supermicro.com>, email Marketing@Supermicro.com or call the San Jose, CA headquarters at +1 408-503-8000.

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